

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	107	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) and (synchroniz\$5 or optimiz\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/09 10:41
L2	51	1 and bridg\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/09 10:41
L3	51	2 and (engag\$3 or disengag\$3 or enabl\$3 or disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/09 10:42
S1	1	10/660161	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 15:10
S2	49	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3) and bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 15:11
S3	51	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat) and bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 15:12
S4	55	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) and bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 15:27
S5	21	S4 and (control\$3 and disabl\$3 )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 15:52
S6	55	S4 and (control\$3 and (disabl\$3 or enabl\$3) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:51

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S7	0	@rlad<"20030911" and (( parallel with (cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3)) and bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:40
S8	10	@rlad<"20030911" and ( parallel with (cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 17:22
S9	106	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) and (disabl\$3 or enabl\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/09 10:40
S10	87	S9 and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 17:23
S11	56	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) and ((disabl\$3 or enabl\$3) with signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 17:46
S12	3	10/463057	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 17:26
S13	2	09/985286	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/06 17:32
S14	13	@rlad<"20030911" and ((dynamic\$5 or parallel) with (cosimulat\$3 or co-simulat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 09:26
S15	1	"5771370".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 10:51
S16	1	10/834896	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 10:51

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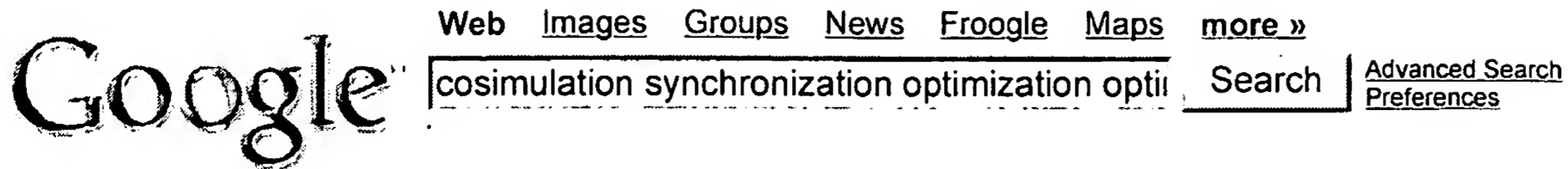
S17	13	@rlad<"20030911" and ((parallel or dynamic\$5) with (cosimulat\$3 or co-simulat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:49
S19	2	09/058726	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:42
S20	2	09/058726	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:43
S21	3294	@rlad<"20030911" and ((parallel or dynamic\$5) with (cosimulat\$3 or co-simulat\$3 or simulat\$3)) and process	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 17:17
S22	226	S21 and (shar\$3 with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:50
S23	205	S22 and (control\$3 and (disabl\$3 or enabl\$3) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 17:17
S24	97	S22 and (simulator )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:51
S25	88	S24 and (control\$3 and (disabl\$3 or enabl\$3) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:57
S26	30	S25 and bridge	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/09 10:41
S27	0	S21 and (control\$3 with optimiz\$5 with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:58

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S28	1260	S21 and (control\$3 with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:58
S29	0	S21 and (control\$3 with optimiz\$5 )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:58
S30	0	S21 and ( optimiz\$5 with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:58
S31	985	S28 and (control\$3 and (disabl\$3 or enabl\$3) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:59
S32	160	S28 and (control\$3 and (disabl\$3 with signal) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:59
S33	115	S28 and (control\$3 with(disabl\$3 with signal) )	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 12:59
S34	10	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) same bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 15:33
S36	0	@rlad<"20030911" and ((cosimulat\$3 or co-simulat\$3 or coverificat\$3 or co-verificat\$3 or coverif\$3 or co-verif\$3) with bridg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 15:28
S38	137	pci with bidirection\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 15:31
S39	10	S34 and (disabl\$3 or enabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 15:33

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S40	572	@riad<"20030911" and ((parallel or dynamic\$5) with (cosimulat\$3 or co-simulat\$3 or simulat\$3)) and process and bridge	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 17:17
S41	100	S22 and (control\$3 and (disabl\$3 or enabl\$3 or stop\$4 or start\$3) with process)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/08 17:18

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**optimization** cross loop boundaries, affect function calls and ... third one is an idle instruction that is used to **synchronize** ...

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3.1 BX\_SIM\_ID When using **cosimulation** it has something to do with which ... 8

**Optimization** Ideas 8.1 Traces. "Almost all programming can be viewed as an ...

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**Synchronization** with external events: **Synchronization** of device drivers with ...

That is, the **optimization** is more likely to be effective with respect to ...

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symbolic traces (represented as a Bus and **Synchronization** Event ... analysis tool into a design exploration/**optimization** tool for SOC ...

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Design Space Exploration; Parameter **Optimization**; IP-Interface validation ...

Functional Validation with **Cosimulation**. **Cosimulation** Manager ...

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[PS] [In Proc. Design Automation and Test Europe \(DATE\), \Omega March ...](#)

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Power estimation and **optimization** techniques for other system-on-chip components,

... During **cosimulation**, when a macro-operation is executed, ...

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MASCOT: A Specification and **Cosimulation** Method Integrating Data and Control ...

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**Cosimulation** with analog/RF designs, refer to the **Cosimulation** section of the ...

Momentum includes simulation, **optimization**, and visualization tools for ...

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Other **optimization** criteria can be included beside schedulability, ... Timed and

untimed hardware/software **cosimulation**: application and efficient ...

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mance-analysis methodology where an initial **cosimulation** of ... formance-analysis  
tool into an **optimization** framework to auto- ...  
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